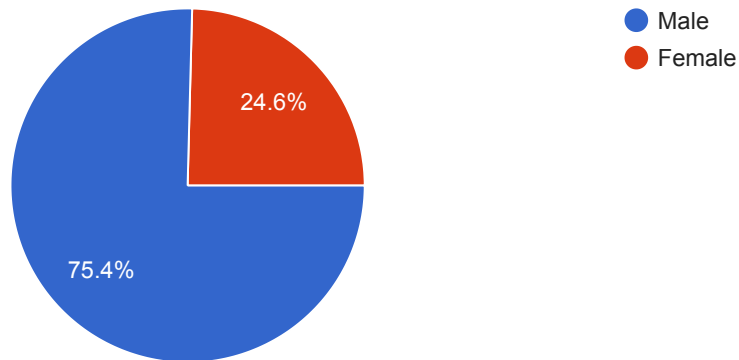


## Gender

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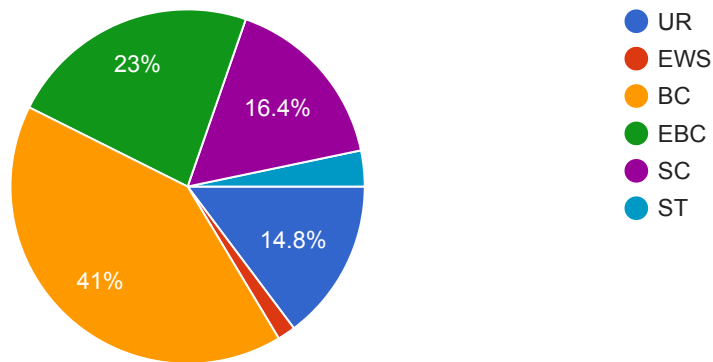
61 responses



## Category

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61 responses

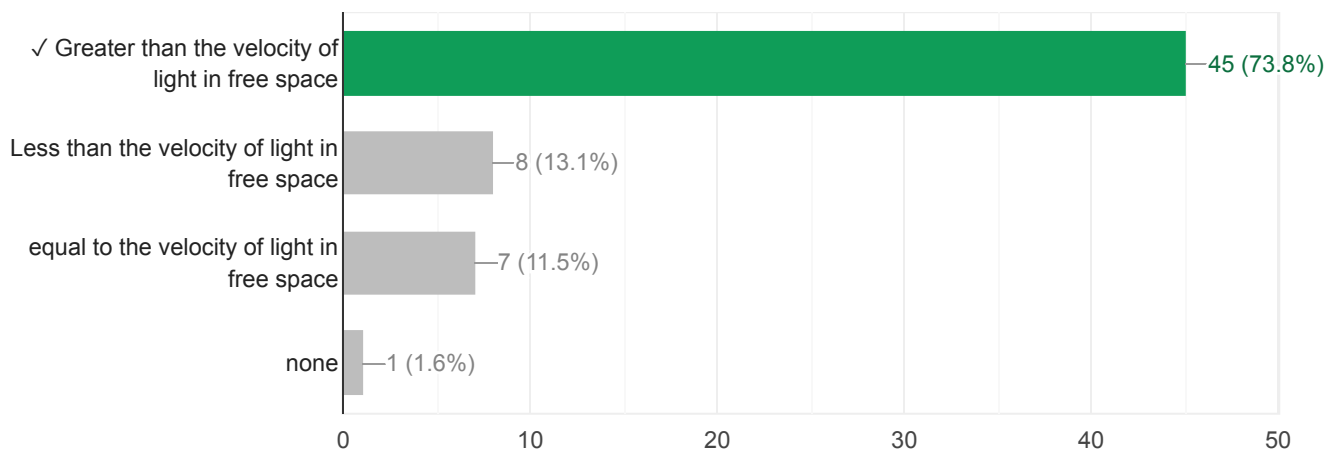


## Online Test for Guest Lecturer (Electronics Engineering)

The phase velocity of waves propagating in a hollow metal waveguide is

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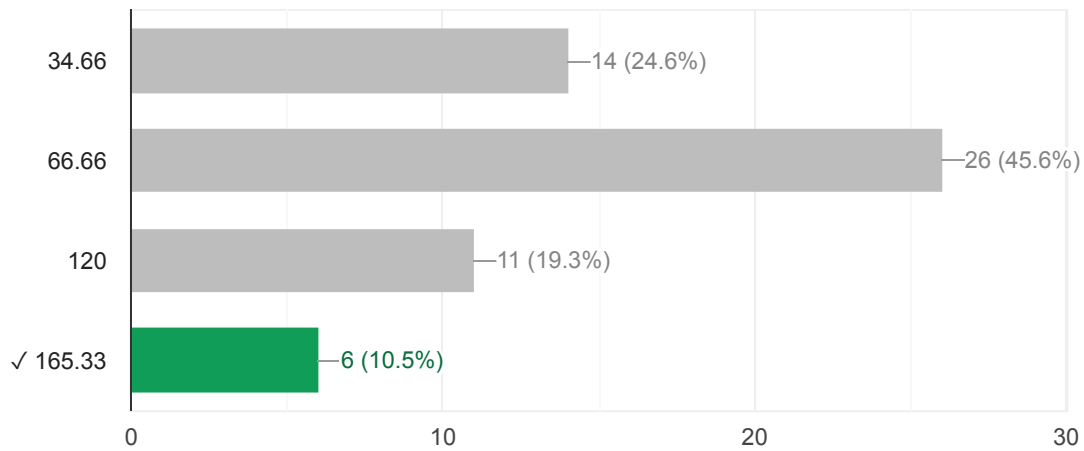
45 / 61 correct responses



In the given below waveform, find energy in (joule)

 Copy

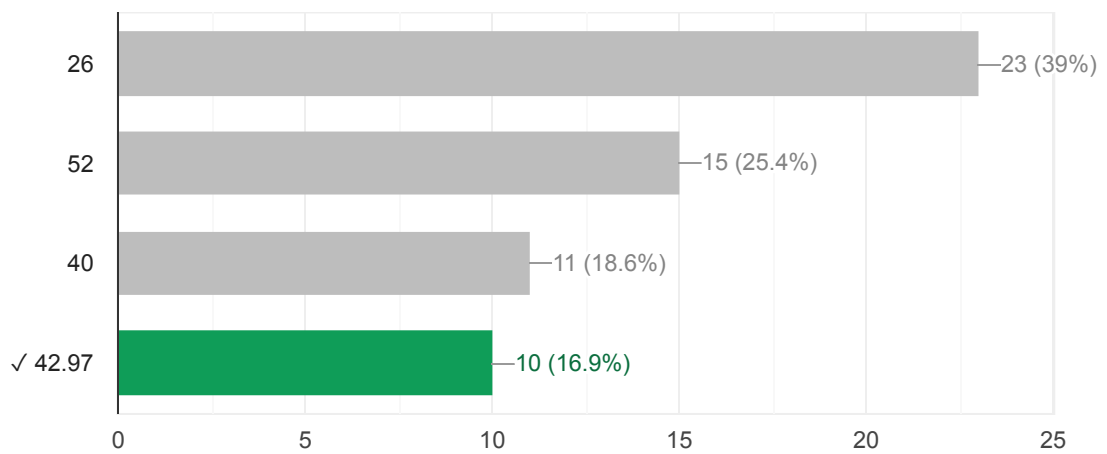
6 / 57 correct responses



If  $x(t) = 4 \cos(2t - \pi/4) + 6 \sin(2t)$ , Find Power of the given equation in watt is

 Copy

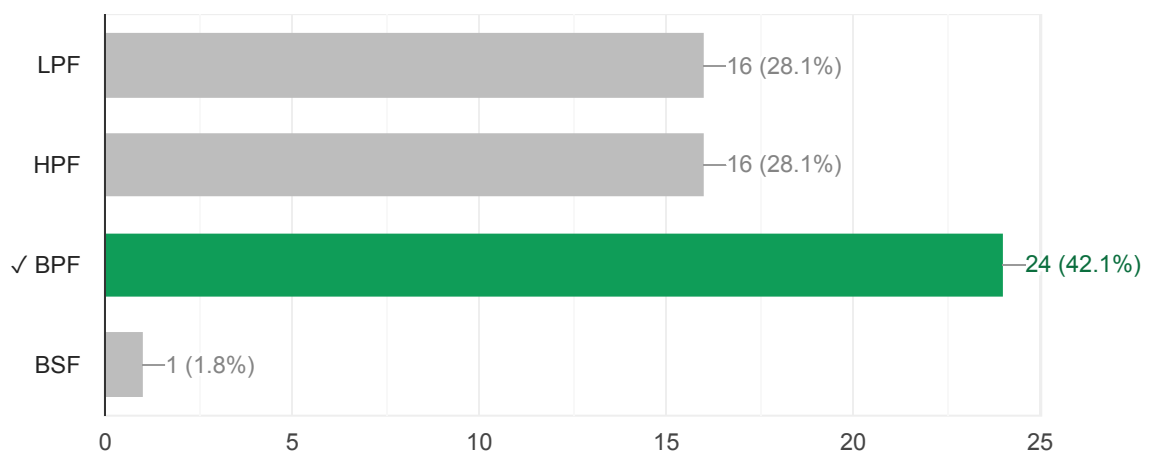
10 / 59 correct responses



An FIR Filter given equation is  $y[n] = 5x[n] - 5x[n-2]$ . The filter can be used to a

 Copy

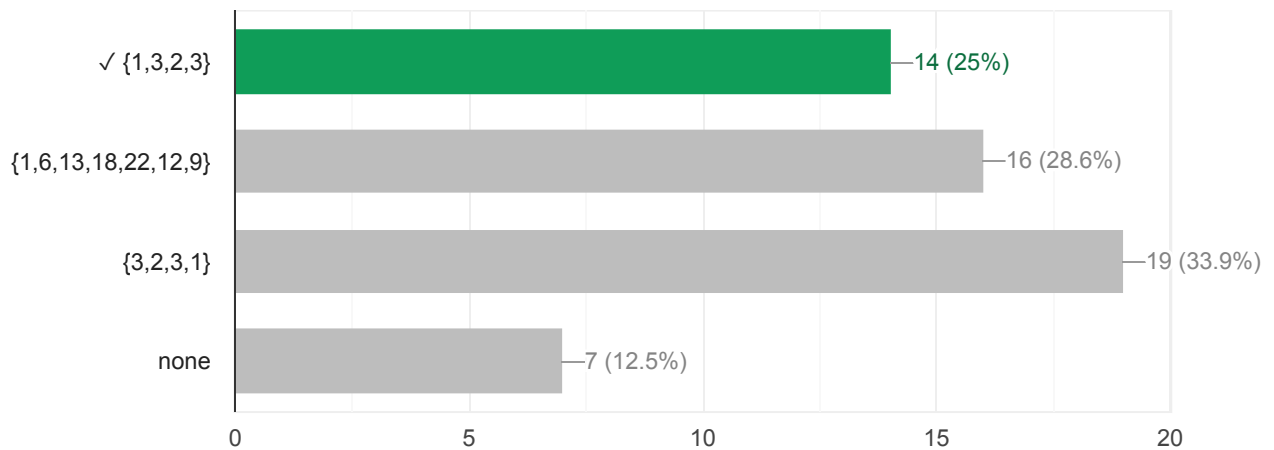
24 / 57 correct responses



If  $x[n] = \{1,3,2,3\}$ ; then find the value of  $[DFT\{DFT x[n]\}]$  is ( $N=4$ )

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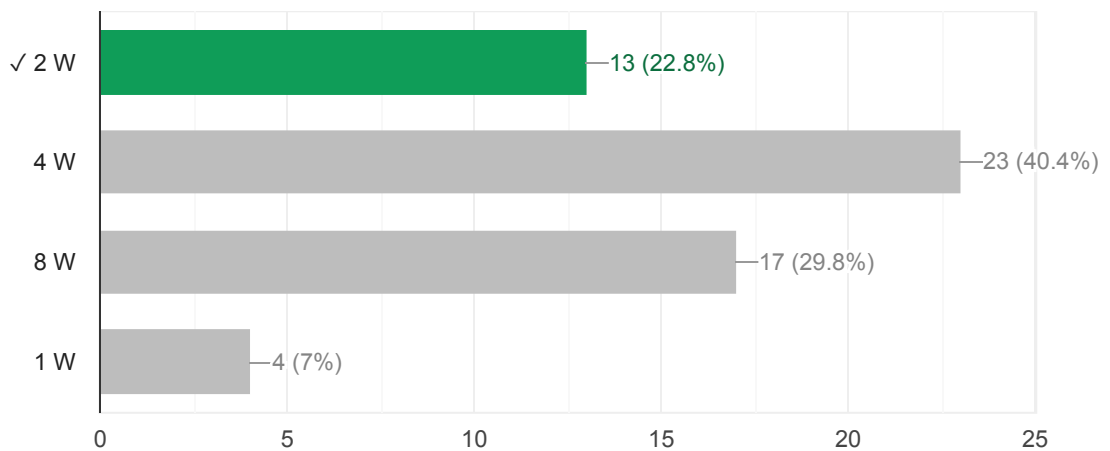
14 / 56 correct responses



Find Power in the given periodic wave form as shown below

 Copy

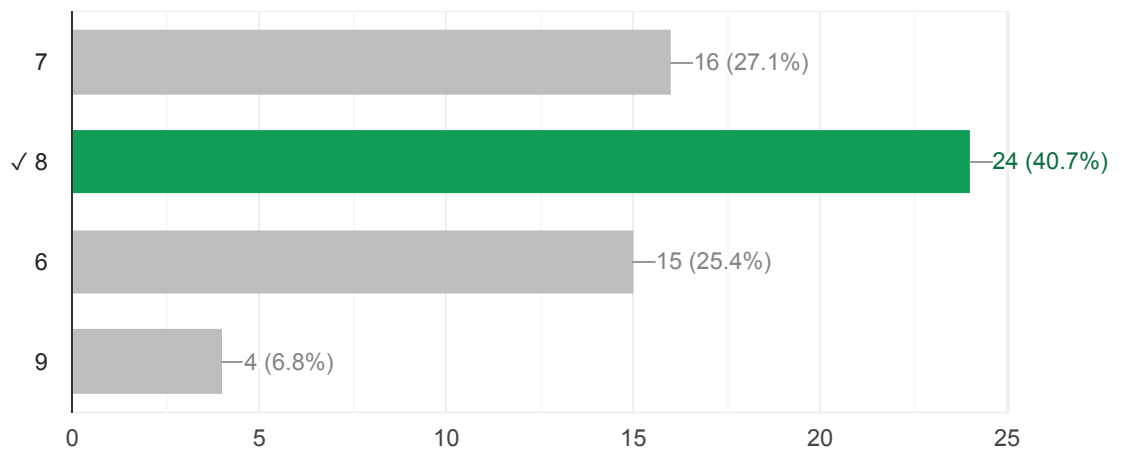
13 / 57 correct responses



Implement  $9 \times 1$  Mux using  $2 \times 1$  Mux then total number of  $2 \times 1$  Mux used

 Copy

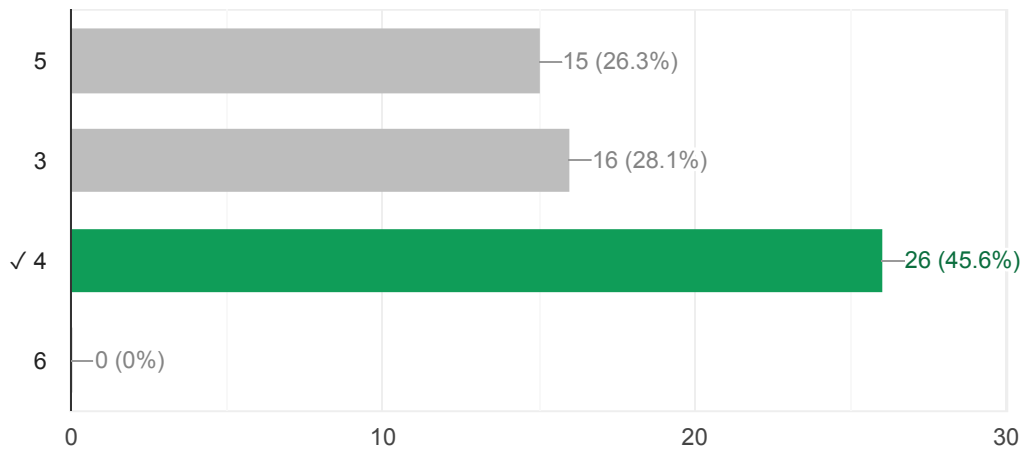
24 / 59 correct responses



If  $F = \sum m(1,5,6,7,11,12,13,15)$  given min terms. Find number of essential prime implicants.



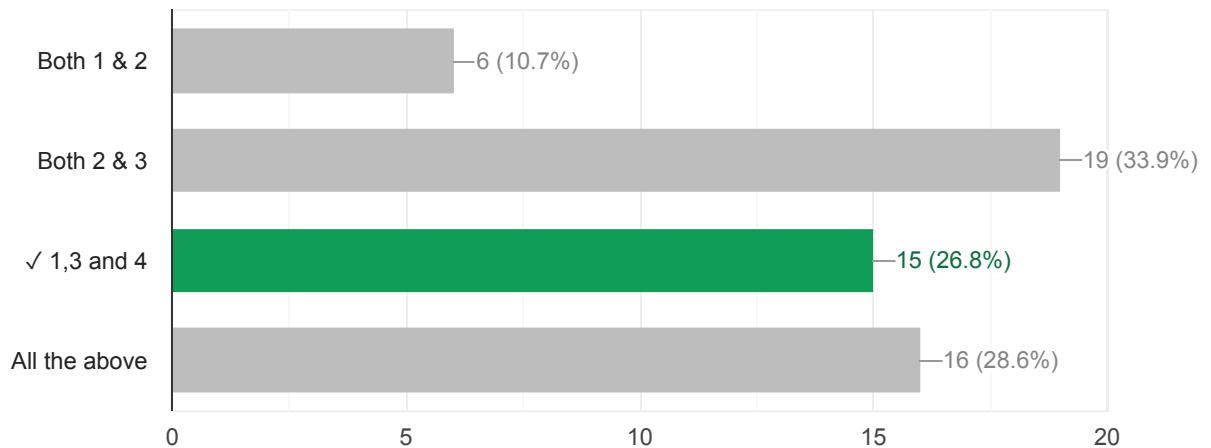
26 / 57 correct responses



Which of the following given below min terms expression are neutral and self dual? 1)  $\sum m(0,2,4,6,8,10,12,14)$ . 2)  $\sum m(3)$ . 3)  $\sum m(0,2)$ . 4)  $\sum m(1,3)$



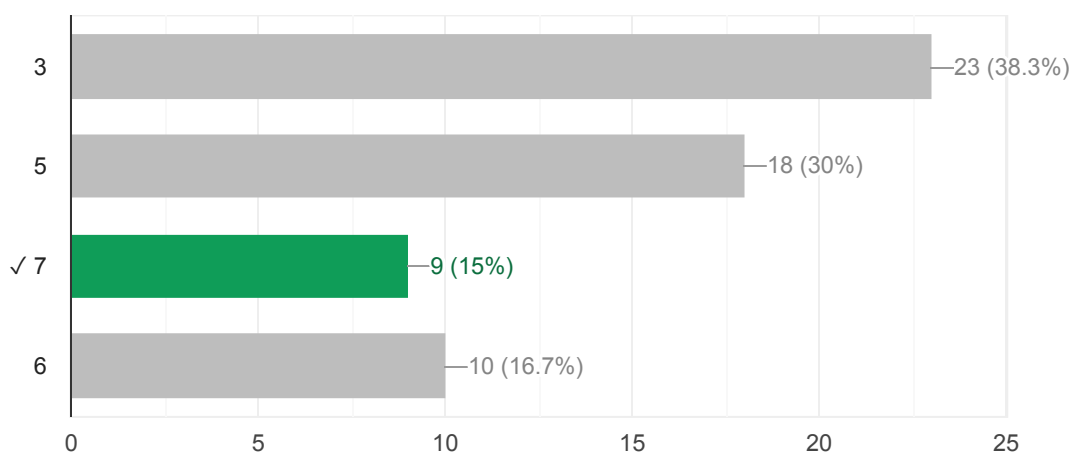
15 / 56 correct responses



How many 2x1 Mux required to implement full adder?



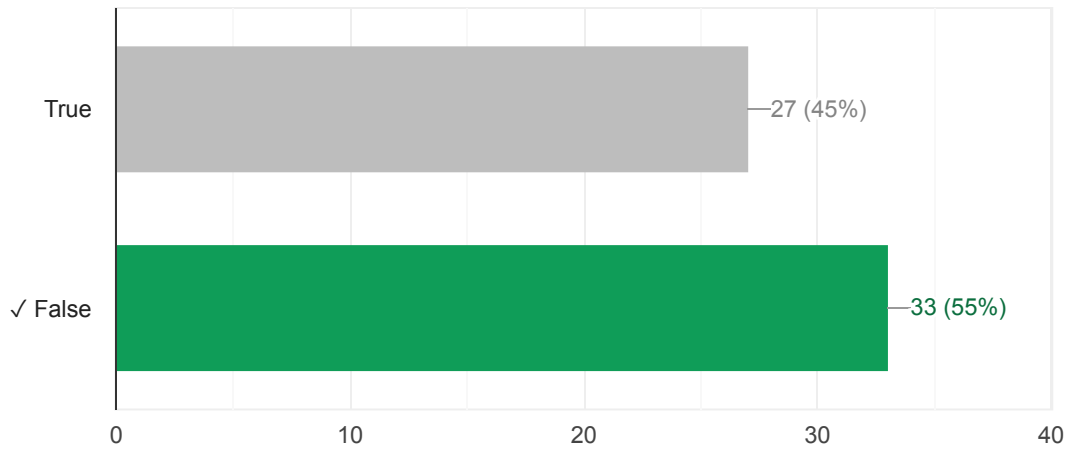
9 / 60 correct responses



If a 7 bit hamming code word received by a receiver is (1011011). Assume the even parity and check whether the received code is correct.



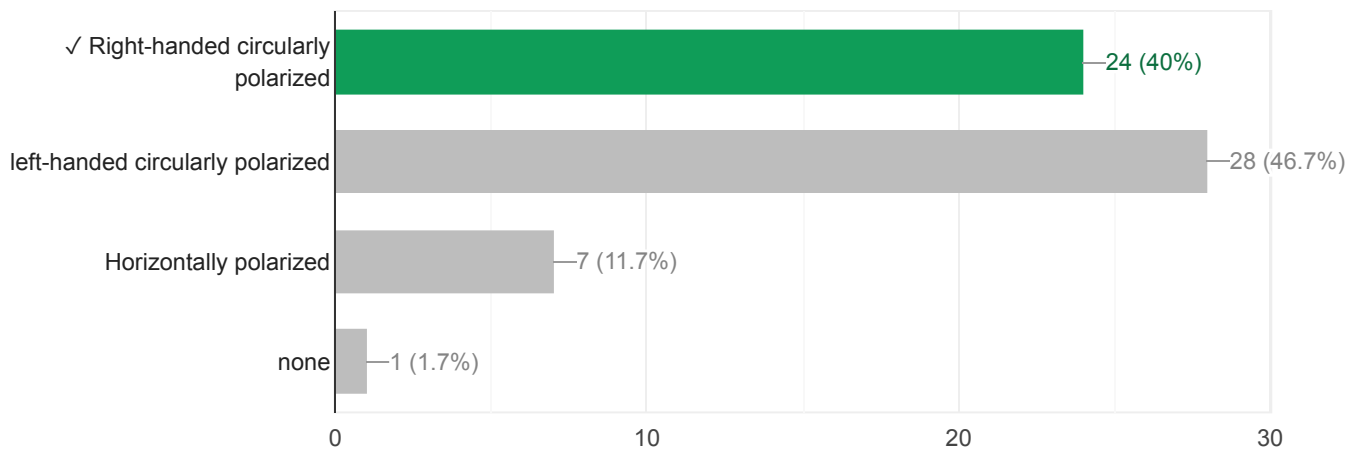
33 / 60 correct responses



If a left-handed circularly polarized wave is incident normally on a plane perfect conductor, then the reflected wave will be



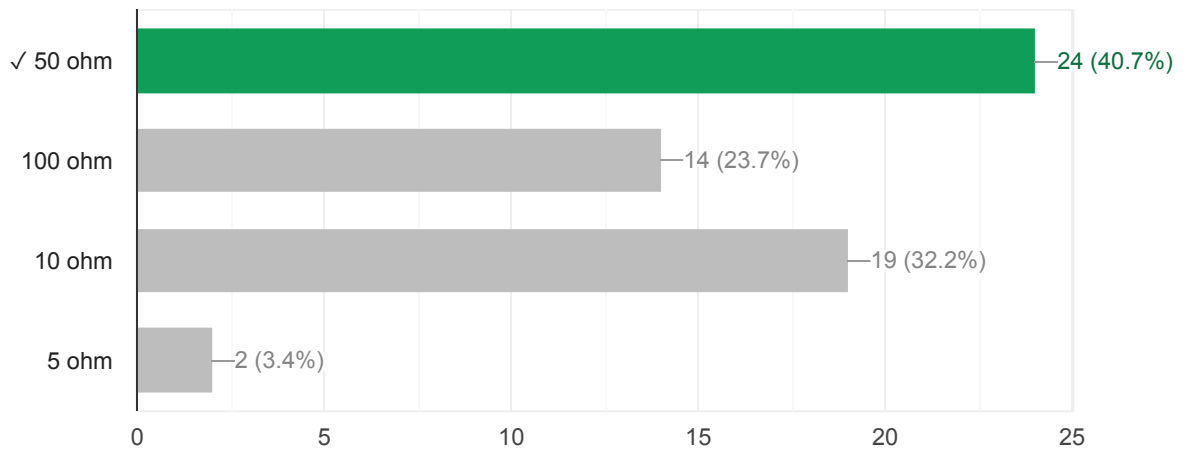
24 / 60 correct responses



In the given circuit looking into nodes 1 and 2 then equivalent resistance is



24 / 59 correct responses

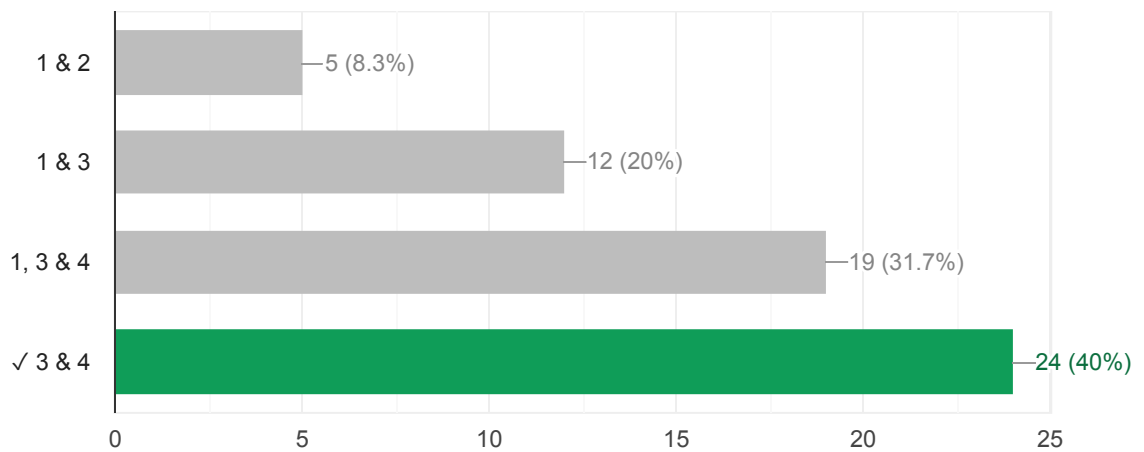


Consider the following statements regarding comparison of FET with BJT: (1) BJT is Less Noisy than FET, (2) FET is a current-controlled device whereas BJT is a voltage-controlled device, (3) FETs are more temperature stable than BJT, (4) FET is simple to fabricate occupy less area on the single chip.



Which of the above statements are correct ?

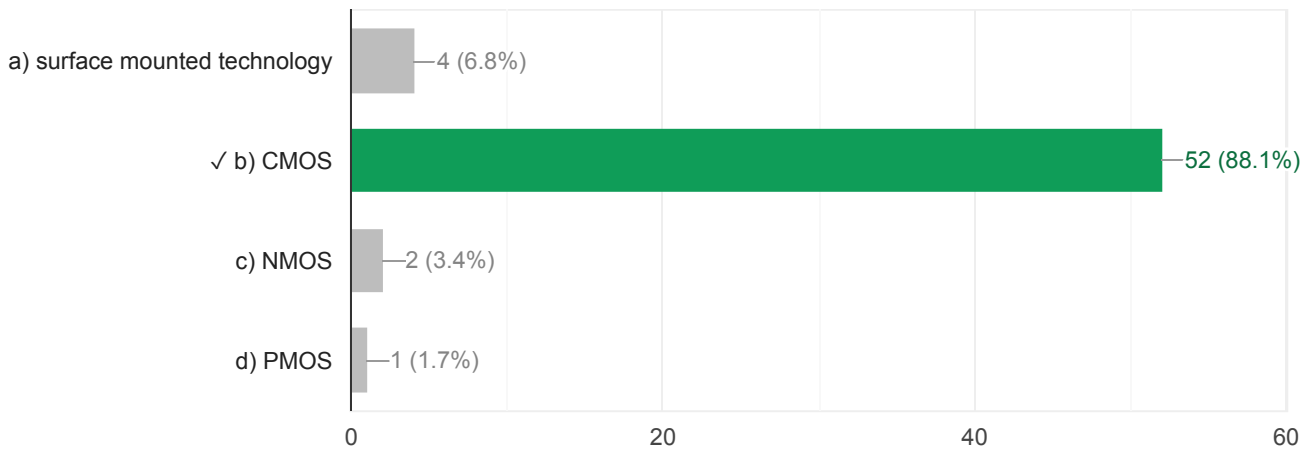
24 / 60 correct responses



which of the following technologies consumes less power



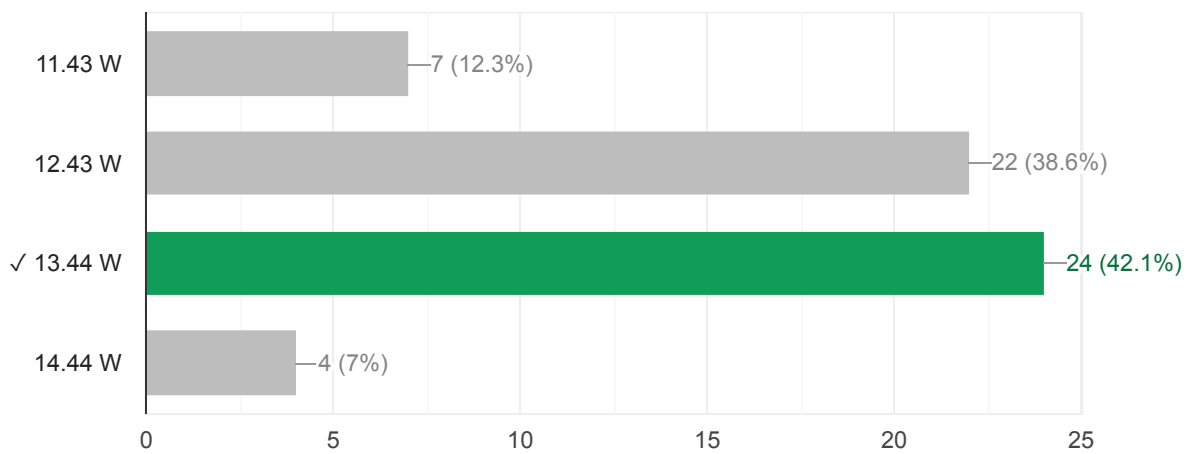
52 / 59 correct responses



What is the maximum power transferred to the load R in the given circuit?



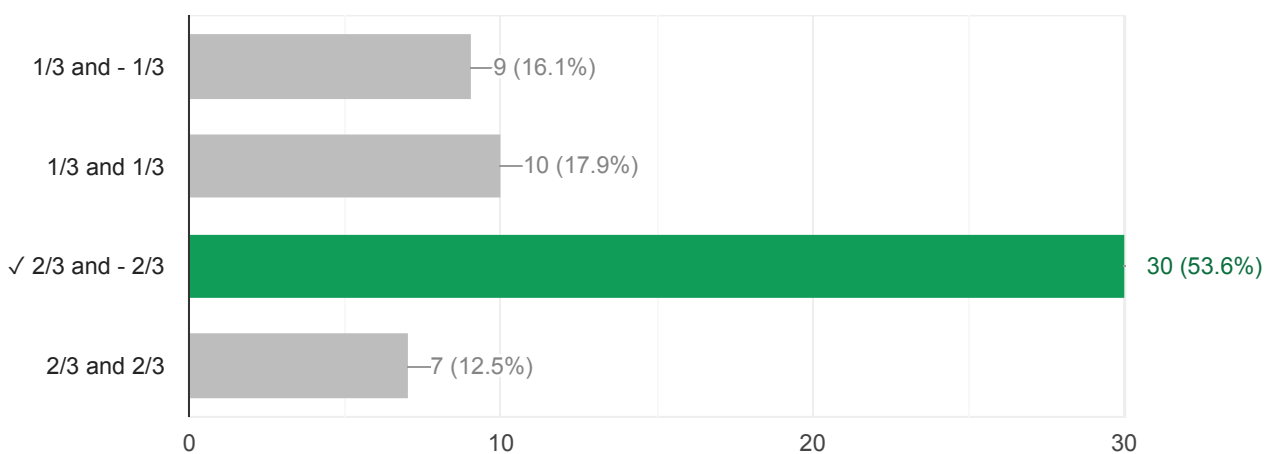
24 / 57 correct responses



What are the open circuit reverse voltage gain and the short circuit forward current gain respectively for the two port network shown in the figure?



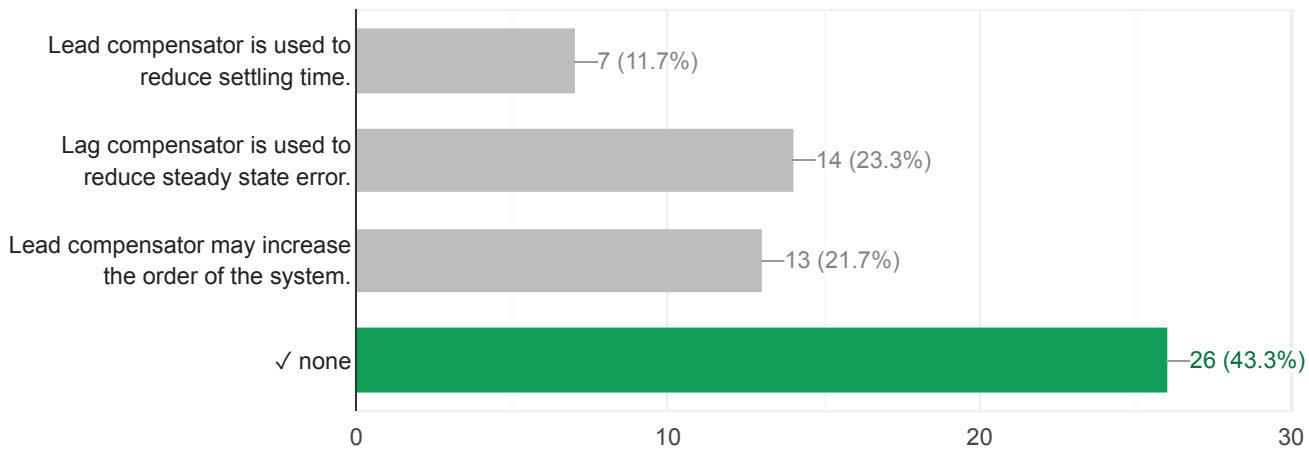
30 / 56 correct responses



Which of the following statement is incorrect?

 Copy

26 / 60 correct responses

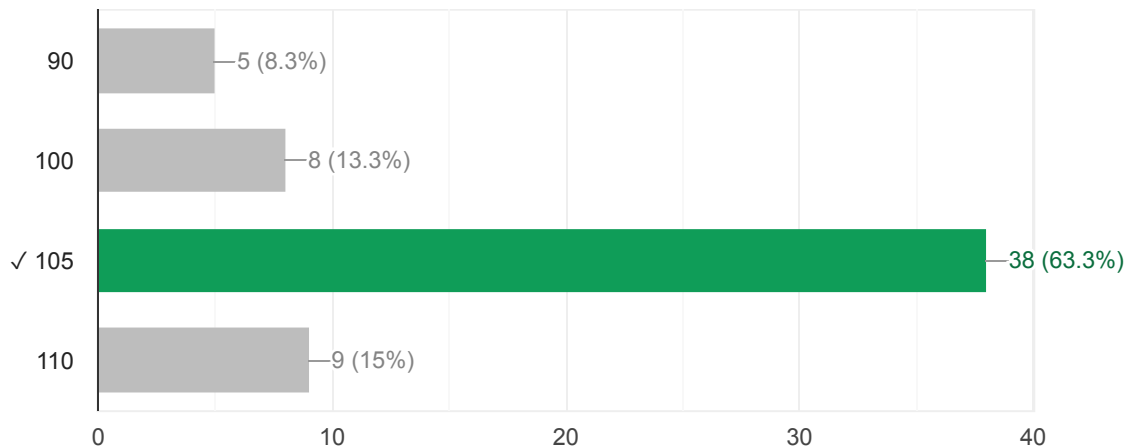


In the given circuit, the silicon transistor has  $\beta=75$  and a collector voltage  $V_c= 9V$

 Copy

Then the ratio of  $R_B$  and  $R_c$  is \_\_\_\_\_.

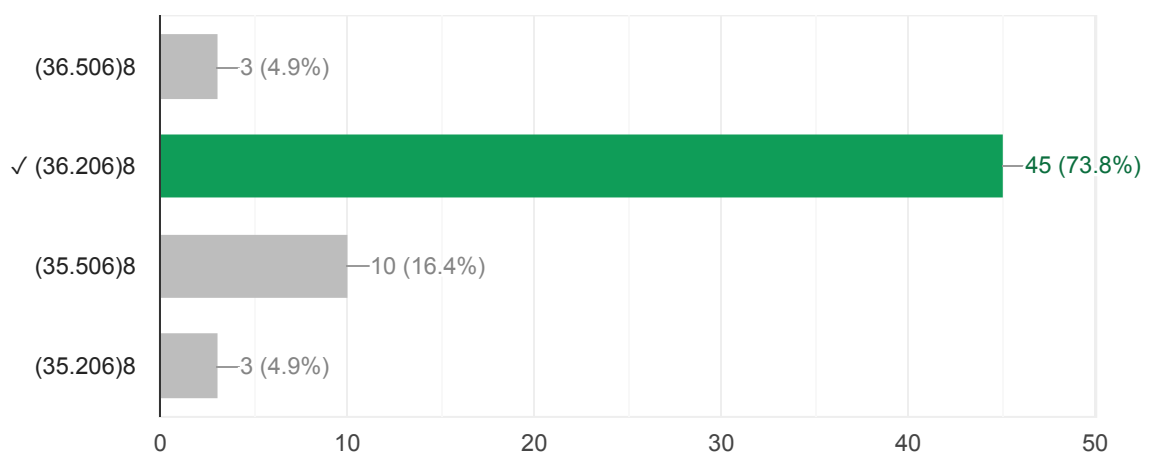
38 / 60 correct responses



The following hexadecimal number  $(1E.43)_{16}$  is equivalent to

 Copy

45 / 61 correct responses





# Online Test for Guest Lecturer (Electronics Engineering)

Common Test for Government Polytechnic, Katihar, Khagaria and Bhagalpur

Date of Test- 31-05-2022, Timing- 11:00 AM to 11:30 AM (Duration= 30 min), Full Marks: 20

## IMPORTANT INSTRUCTIONS:-

1. Test consists of 20 questions carrying 1 mark each.
2. Test consists of Multiple Choice Questions (MCQ) as well as Multiple Select Questions (MSQ); a question may have one correct choice or multiple correct choices.
4. Attempt all questions. There is NO negative marking in any types of questions.
5. Candidates can SUBMIT only 1 Response.
6. Candidates are advised to SUBMIT test on or before 11:30 AM. After that test will be closed.

---

### \* Required

1. Email \*

\_\_\_\_\_

2. Name of the college applied for \*

*Mark only one oval.*

- Government Polytechnic, Katihar
- Government Polytechnic, Khagaria
- Government Polytechnic, Bhagalpur

3. Name of Candidate \*

\_\_\_\_\_

4. Father's/Husband's Name \*

\_\_\_\_\_

5. Date of Birth \*

---

*Example: January 7, 2019*

6. Gender \*

*Mark only one oval.*

Male

Female

7. Category \*

*Mark only one oval.*

UR

EWS

BC

EBC

SC

ST

**Online Test for  
Guest Lecturer  
(Electronics  
Engineering)**

Common Test for Government Polytechnic, Katihar, Khagaria and Bhagalpur

Date of Test- 31-05-2022, Timing- 11:00 AM to 11:30 AM  
(Duration= 30 min), Full Marks: 20

**IMPORTANT INSTRUCTIONS:-**

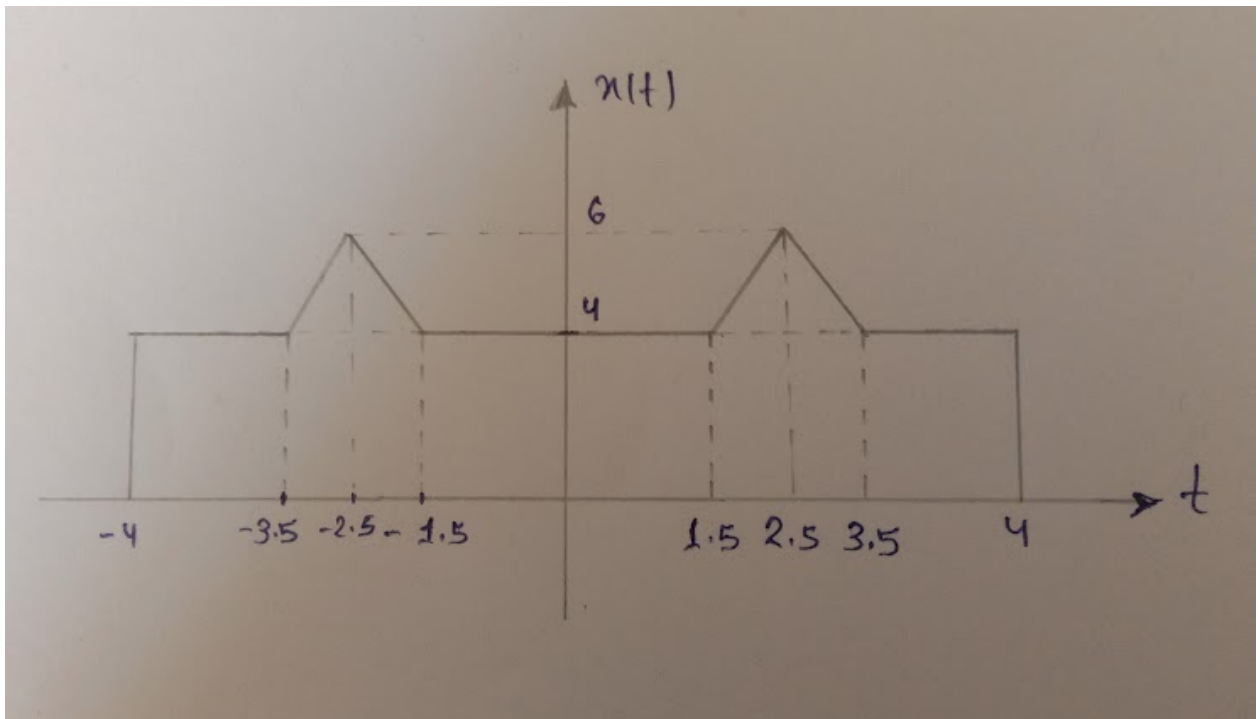
1. Test consists of 20 questions carrying 1 mark each.
2. Test consists of Multiple Choice Questions (MCQ) as well as Multiple Select Questions (MSQ); a question may have one correct choice or multiple correct choices.
4. Attempt all questions. There is NO negative marking in any types of questions.
5. Candidates can SUBMIT only 1 Response.
6. Candidates are advised to SUBMIT test on or before 11:30 AM. After that test will be closed.

8. The phase velocity of waves propagating in a hollow metal waveguide is 1 point

Mark only one oval.

- Greater than the velocity of light in free space
- Less than the velocity of light in free space
- equal to the velocity of light in free space
- none

9. In the given below waveform, find energy in (joule) 1 point



Mark only one oval.

- 34.66
- 66.66
- 120
- 165.33

10. If  $x(t) = 4 \cos (2t - \pi/4) + 6 \sin (2t)$ , Find Power of the given equation in watt is 1 point

*Mark only one oval.*

- 26  
 52  
 40  
 42.97

11. An FIR Filter given equation is  $y[n] = 5 x[n] - 5 x[n-2]$ . The filter can be used to a 1 point

*Mark only one oval.*

- LPF  
 HPF  
 BPF  
 BSF

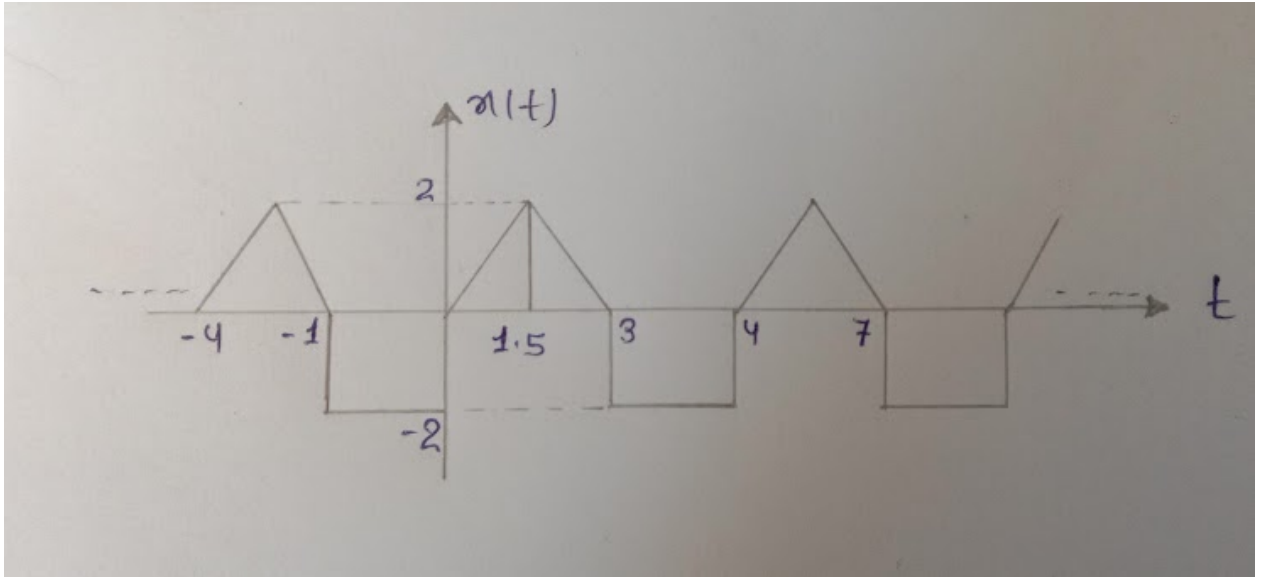
12. If  $x[n] = \{1,3,2,3\}$ ; then find the value of  $[DFT\{DFT x[n]\}]$  is (N=4) 1 point

*Mark only one oval.*

- {1,3,2,3}  
 {1,6,13,18,22,12,9}  
 {3,2,3,1}  
 none

13. Find Power in the given periodic wave form as shown below

1 point



Mark only one oval.

2 W

4 W

8 W

1 W

14. Implement 9x1 Mux using 2x1 Mux then total number of 2x1 Mux used

1 point

Mark only one oval.

7

8

6

9

15. If  $F = \sum m(1,5,6,7,11,12,13,15)$  given min terms. Find number of essential prime implicants. 1 point

*Mark only one oval.*

- 5  
 3  
 4  
 6

16. Which of the following given below min terms expression are neutral and self dual? 1)  $\sum m(0,2,4,6,8,10,12,14)$ . 2)  $\sum m(3)$ . 3)  $\sum m(0,2)$ . 4)  $\sum m(1,3)$  1 point

*Mark only one oval.*

- Both 1 & 2  
 Both 2 & 3  
 1,3 and 4  
 All the above

17. How many 2x1 Mux required to implement full adder? 1 point

*Mark only one oval.*

- 3  
 5  
 7  
 6

18. If a 7 bit hamming code word received by a receiver is (1011011). Assume the even parity and check whether the received code is correct. 1 point

*Mark only one oval.*

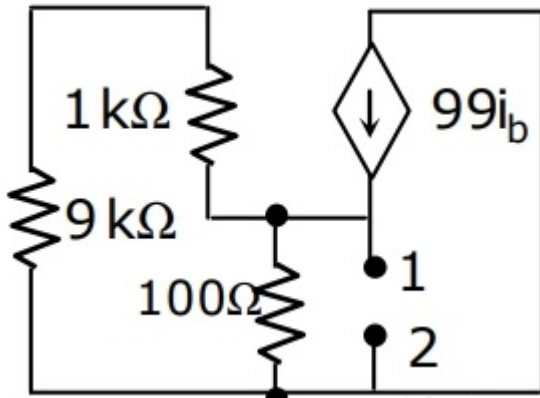
- True  
 False

19. If a left-handed circularly polarized wave is incident normally on a plane perfect conductor, then the reflected wave will be 1 point

*Mark only one oval.*

- Right-handed circularly polarized  
 left-handed circularly polarized  
 Horizontally polarized  
 none

20. In the given circuit looking into nodes 1 and 2 then equivalent resistance is 1 point  
is



Mark only one oval.

- 50 ohm  
 100 ohm  
 10 ohm  
 5 ohm

21. Consider the following statements regarding comparison of FET with BJT: 1 point  
 (1) BJT is Less Noisy than FET, (2) FET is a current-controlled device whereas BJT is a voltage-controlled device, (3) FETs are more temperature stable than BJT, (4) FET is simple to fabricate occupy less area on the single chip.

Which of the above statements are correct ?

Mark only one oval.

- 1 & 2  
 1 & 3  
 1, 3 & 4  
 3 & 4



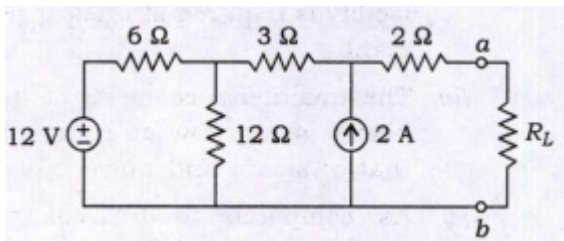
22. which of the following technologies consumes less power

1 point

Mark only one oval.

- a) surface mounted technology
- b) CMOS
- c) NMOS
- d) PMOS

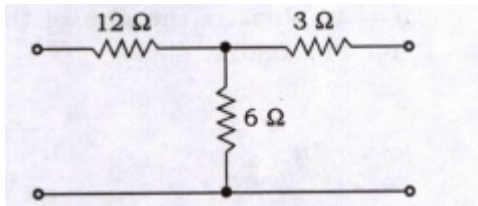
23. What is the maximum power transferred to the load R in the given circuit? 1 point



Mark only one oval.

- 11.43 W
- 12.43 W
- 13.44 W
- 14.44 W

24. What are the open circuit reverse voltage gain and the short circuit forward current gain respectively for the two port network shown in the figure? 1 point



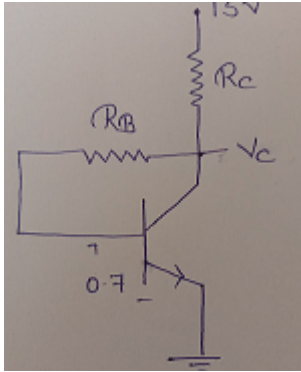
Mark only one oval.

- 1/3 and - 1/3
  - 1/3 and 1/3
  - 2/3 and - 2/3
  - 2/3 and 2/3
25. Which of the following statement is incorrect? 1 point

Mark only one oval.

- Lead compensator is used to reduce settling time.
- Lag compensator is used to reduce steady state error.
- Lead compensator may increase the order of the system.
- none

26. In the given circuit, the silicon transistor has  $\beta=75$  and a collector voltage  $V_c=9V$ . Then the ratio of  $R_B$  and  $R_c$  is \_\_\_\_\_.



Mark only one oval.

- 90
- 100
- 105
- 110

27. The following hexadecimal number  $(1E.43)_{16}$  is equivalent to \_\_\_\_\_

1 point

Mark only one oval.

- $(36.506)_8$
- $(36.206)_8$
- $(35.506)_8$
- $(35.206)_8$

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